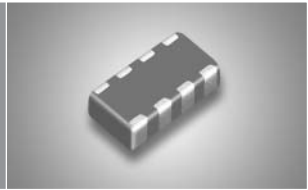


Multilayer Chip Capacitor Array CA Series



► Features

- Cost down solution in SMD and material.
- Compact size for 4 capacitors in one package (8P4C).
- Multilayer block structure provides high reliability.
- Excellent solderability with plating electrode.
- RoHS compliance.

► Applications

Applications for I/O port for lap top, desk top computer (RS232, 1284, PS2, USB,...), panel link circuit (TFT-LCD monitor & panel), decoupling bypass circuit for IC, and signal termination etc.

► How to Order

CA41 **N** **220** **K** **050** **T**

1
2
3
4
5
6

1 Series Type: MLCC Array

2 Temperature Coefficient:

N=NPO : 0 ± 30 (ppm/°C)

X=X7R : ± 15 %

Y=Y5V : $+22\% \sim -85\%$

3 Capacitance Value:

Value : $XX \times 10^N \rightarrow XXN$

ex : $22\text{pF} = 22 \times 10^0 \rightarrow 220$

4 Capacitance tolerance

Code	Capacitance Tolerance
J	$\pm 5\%$
K	$\pm 10\%$
M	$\pm 20\%$
Z	$+80\% / -20\%$

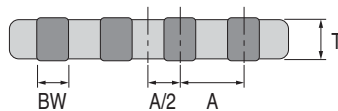
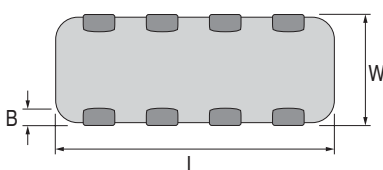
5 Rated Voltage

Code	Rated Voltage(V)
050	50
025	25
016	16

5 Package Type

T = Reel

► Dimensions



Unit:mm

Size (EIA)	3216 (1206)
L	3.20 ± 0.15
W	1.60 ± 0.15
T	0.82 ± 0.10
BW	0.40 ± 0.10
B	0.35 ± 0.15
A	0.80 ± 0.10

Multilayer Chip Capacitor Array – CA Series

► Specifications

1. Capacitance range

Capacitance (pF)	Temperature Characteristics	NPO	X7R	Y5V
10				
15				
22				
33				
47				
56				
68				
82				
100				
120				
150				
180				
220				
330				
470				
1,000				
10,000				
100,000				

2. Capacitance Temperature Characteristic

Dielectric	Temperature Range	Capacitance Change
NPO	-55°C ~ +125°C	0±30ppm/°C
X7R	-55°C ~ +125°C	Within 15%
Y5V	-30°C ~ +85°C	Within +22% ~ -82%

3. Solderability

More than 95% of the terminal surface is to be soldered newly, so metal part does not come out or dissolve.

Solder Temperature	245°C±5°C
Dip Time	5±0.5 sec.

► Package

Standard packing quantity

Size (EIA)	3216 (1206)
Quantity (pcs/reel)	5,000pcs